

**IN THE CLAIMS**

Please amend the claims as follows:

1. (Currently Amended) A microchip for at least one personal computer configured to operate with at least one other computer connected to at least one network, comprising:

at least one internal firewall, said internal firewall configured to deny access to at least a portion of said microchip from said network;

at least one microprocessor with at least one control unit and at least two processing units, said control unit including means for at least one user of said personal computer to control said at least two processing units;

at least one power management component; and

a plurality of dies, at least two of [the] said dies made by a separate fabrication process and assembled into a package with separate die sections connected directly.

~~wherein at least one edge of at least one of the dies has any shape or pattern that is not a~~  
~~straight line~~

2-24. (Cancelled)

25. (New) The microchip of Claim 1, wherein said internal firewall is configured to allow and/or deny access to portions of said microchip both to at least one user of said personal computer and to at least one user of said microchip from said network during shared use of said microchip.

26. (New) The microchip of Claim 1, wherein said microchip includes at least one photovoltaic cell.

27. (New) The microchip of Claim 1, wherein said microchip includes at least one Faraday Cage.

28. (New) The microchip of Claim 1, wherein said microchip includes at least one photovoltaic cell and at least one Faraday Cage.

29. (New) The microchip of Claim 1, wherein said microchip includes a plurality of Faraday Cages.

30. (New) The microchip of Claim 1, wherein said microchip includes a plurality of internal firewalls.

31. (New) The microchip of Claim 1, wherein said microchip includes at least one radio component and/or at least one microlaser.

32. (New) A microchip for at least one personal computer configured to operate with at least one other computer connected to at least one network, comprising:

a plurality of internal firewalls, said internal firewalls configured to deny access to at least a plurality of portions of said microchip from said network;

at least one microprocessor with at least one control unit and at least two processing units, said control unit including means for at least one user of said personal computer to control said at least two processing units;

at least one power management component; and

a plurality of dies, at least two of said dies made by a separate fabrication process and assembled into a package with separate die sections connected directly.

33. (New) The microchip of Claim 32, wherein said internal firewall is configured to allow and/or deny access to portions of said microchip both to at least one user of said personal computer and to at least one user of said microchip from said network during shared use of said microchip.

34. (New) The microchip of Claim 32, wherein said microchip includes at least one photovoltaic cell.

35. (New) The microchip of Claim 32, wherein said microchip includes at least one Faraday Cage.

36. (New) The microchip of Claim 32, wherein said microchip includes at least one photovoltaic cell and at least one Faraday Cage.

37. (New) The microchip of Claim 32, wherein said microchip includes a plurality of Faraday Cages.

38. (New) A microchip for at least one personal computer configured to operate with at least one other computer connected to at least one network, comprising:

at least one internal firewall, said internal firewall configured to deny access to at least a portion of said microchip from said network;

at least one microprocessor with at least one control unit and at least two processing units, said control unit including means for at least one user of said personal computer to control said at least two processing units;

at least one Faraday Cage;

at least one power management component; and

a plurality of dies, at least two of said dies made by a separate fabrication process and assembled into a package with separate die sections connected directly.

39. (New) The microchip of Claim 38, wherein said internal firewall is configured to allow and/or deny access to portions of said microchip both to at least one user of said personal computer and to at least one user of said microchip from said network during shared use of said microchip.

40. (New) The microchip of Claim 38, wherein said microchip includes at least one photovoltaic cell.

41. (New) The microchip of Claim 38, wherein said microchip includes at least one photovoltaic cell and at least one Faraday Cage.

42. (New) The microchip of Claim 38, wherein said microchip includes a plurality of Faraday Cages.

43. (New) The microchip of Claim 38, wherein said microchip includes a plurality of internal firewalls.